

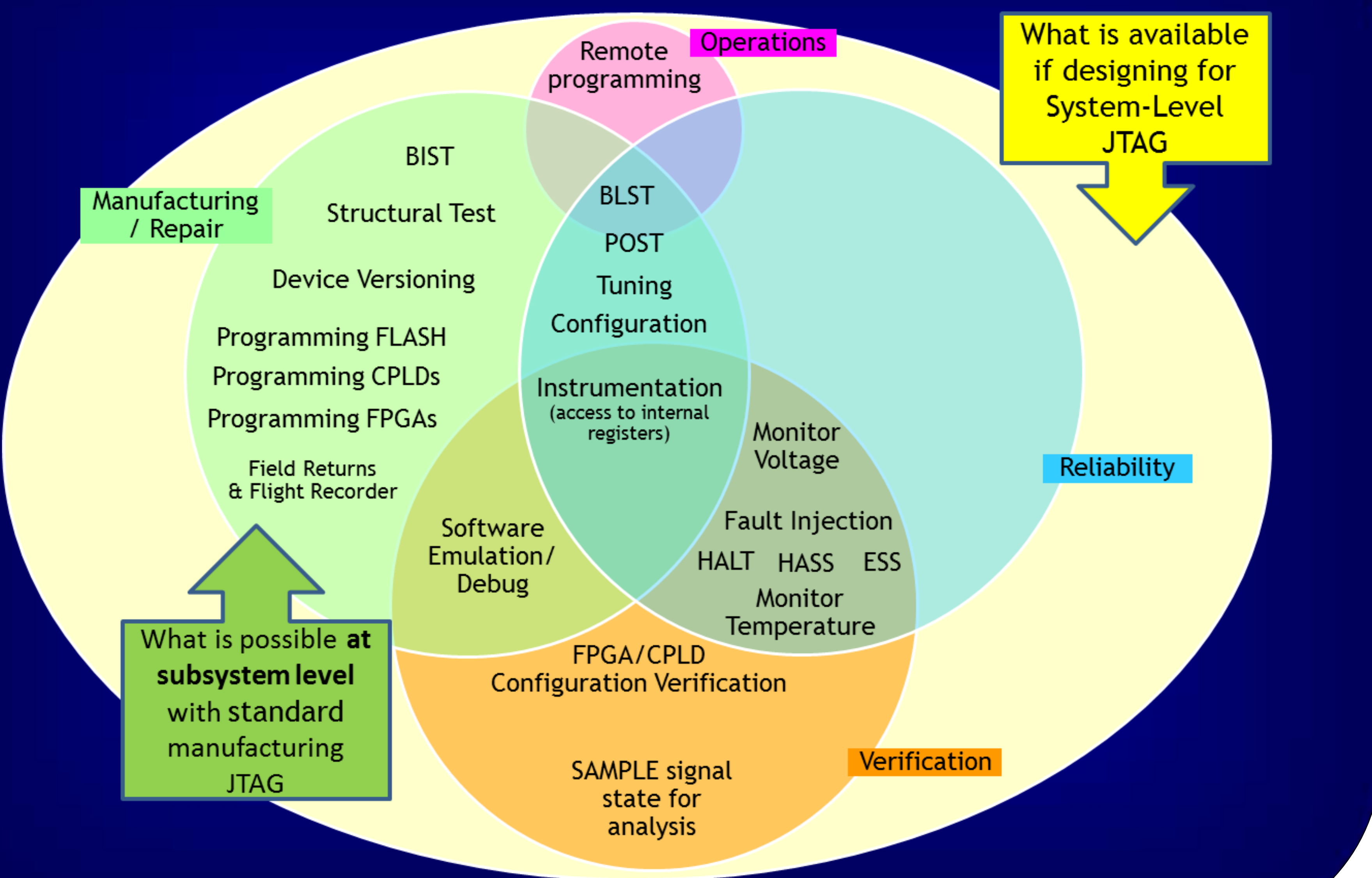
# System Test Access Management (IEEE P2654 / STAM)



www.sjtag.org



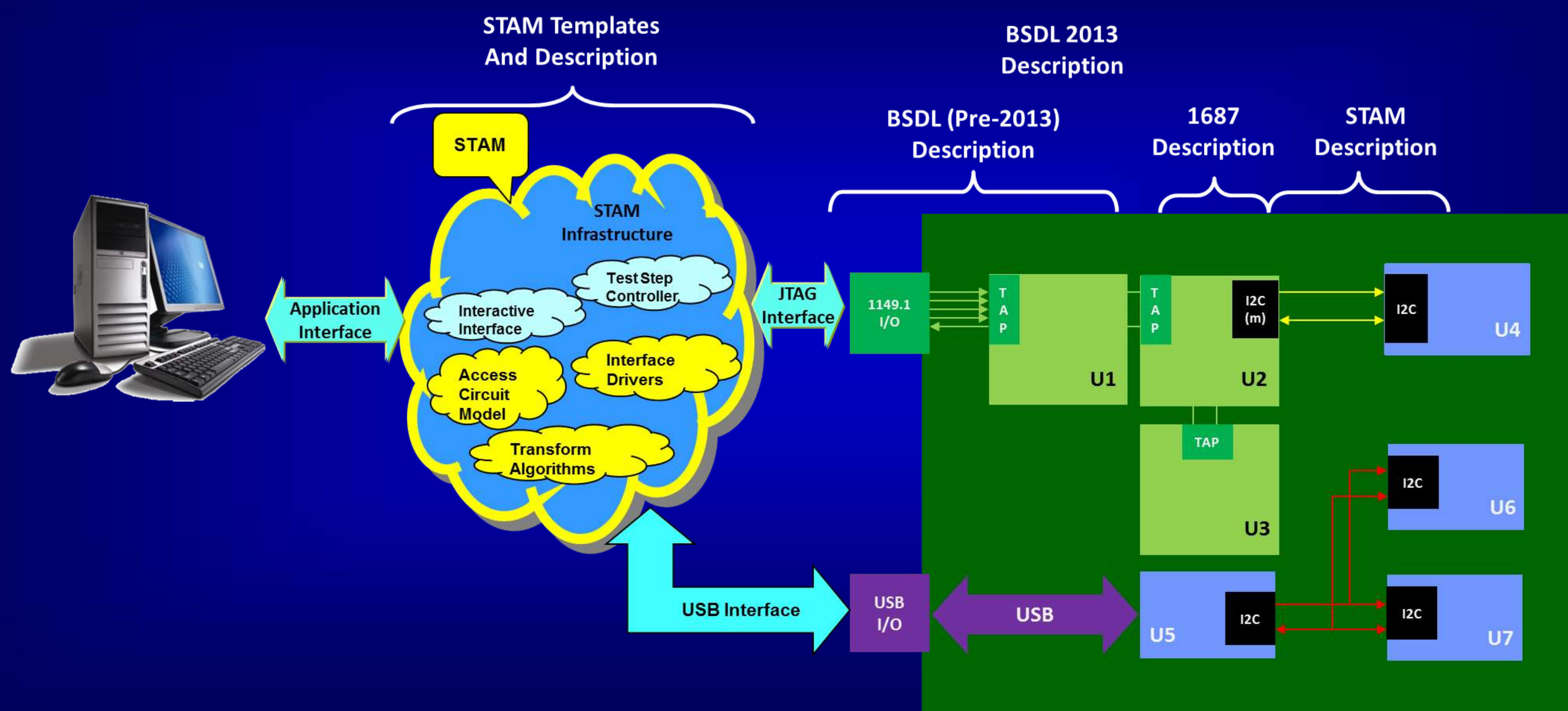
## SJTAG Universe: Use cases in many domains



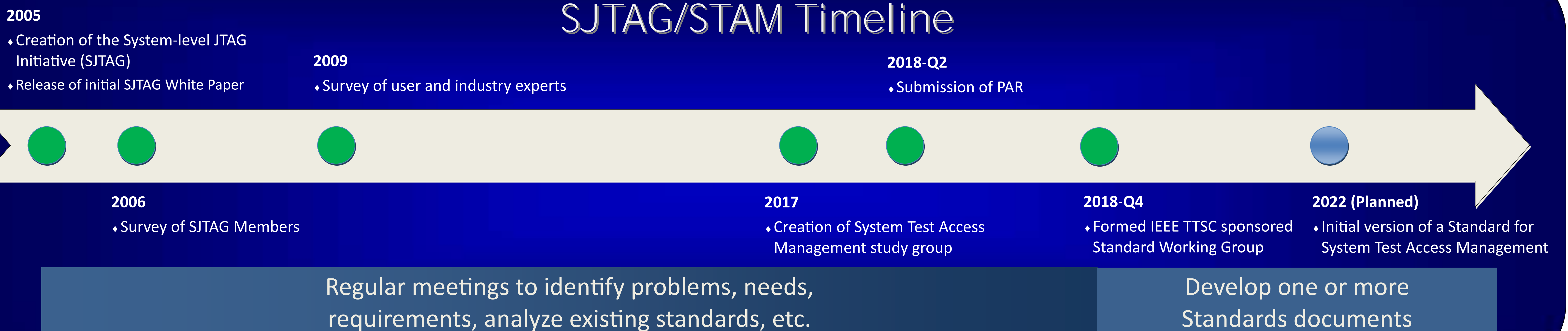
## Concerns to Address

- ❖ Access to target (system topology)
- ❖ Data collection
- ❖ Data Analysis
- ❖ Diagnostics support
- ❖ Hand-off to other standards
- ❖ Test re-use through system hierarchy
- ❖ Black-box vs White-box test
- ❖ Security considerations

## Board test use model: JTAG and USB



## SJTAG/STAM Timeline



## Participating Organisations:

Ampere Computing ❖ Arm ❖ ASSET InterTech ❖ A. T. E. Solutions ❖ Cisco Systems ❖ Curtiss-Wright ❖ Dell ❖ DFT Solutions ❖ Firecron Ltd. ❖ GOPEL Electronics ❖ Intel Corporation ❖ INTELLITECH Inc. ❖ JTAG Technologies ❖ Leonardo ❖ Lund University ❖ Marvell Inc. ❖ National Instruments ❖ Nvidia ❖ NXP Semiconductors ❖ Schweitzer Engineering Laboratories, Inc.