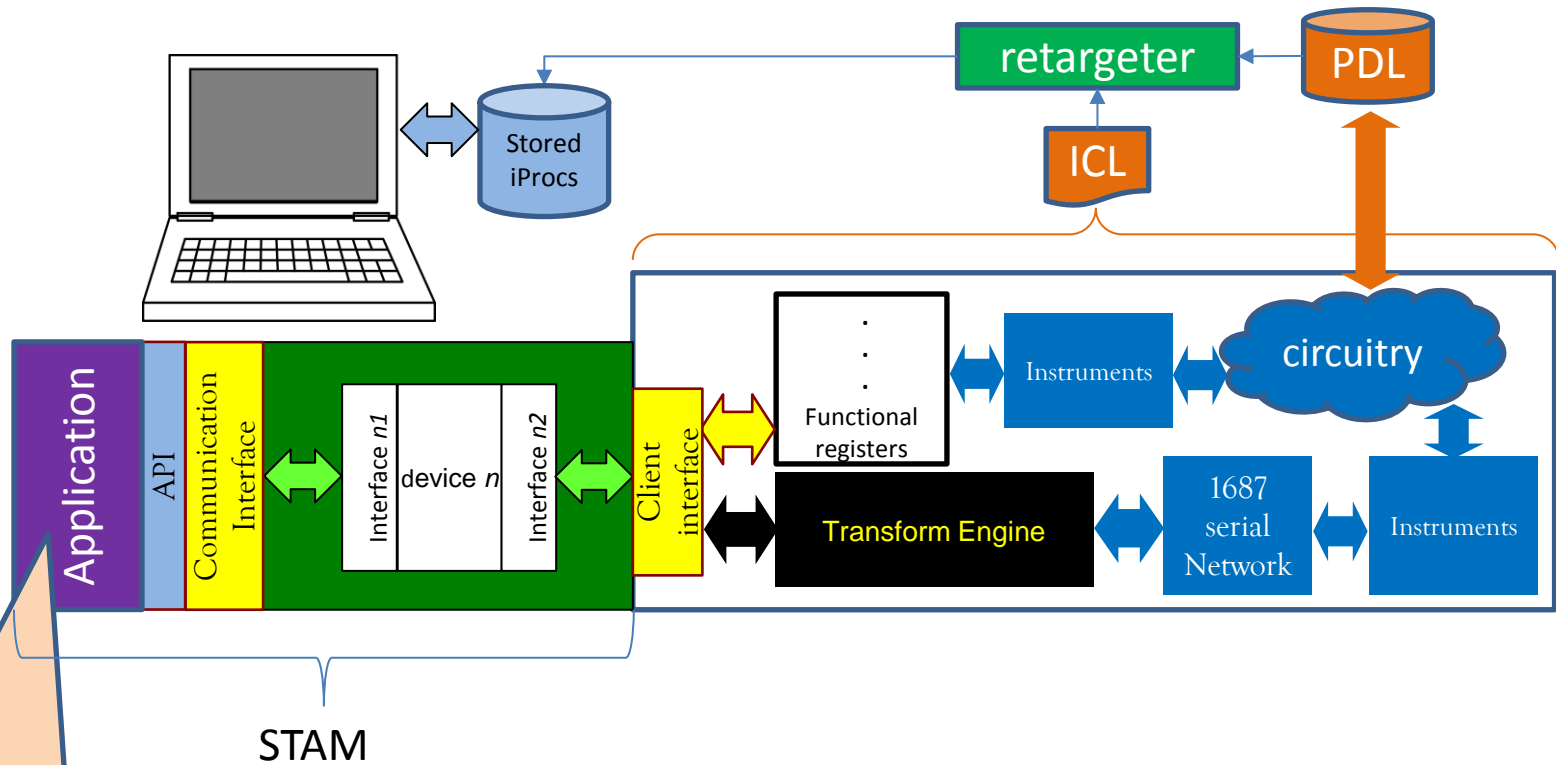
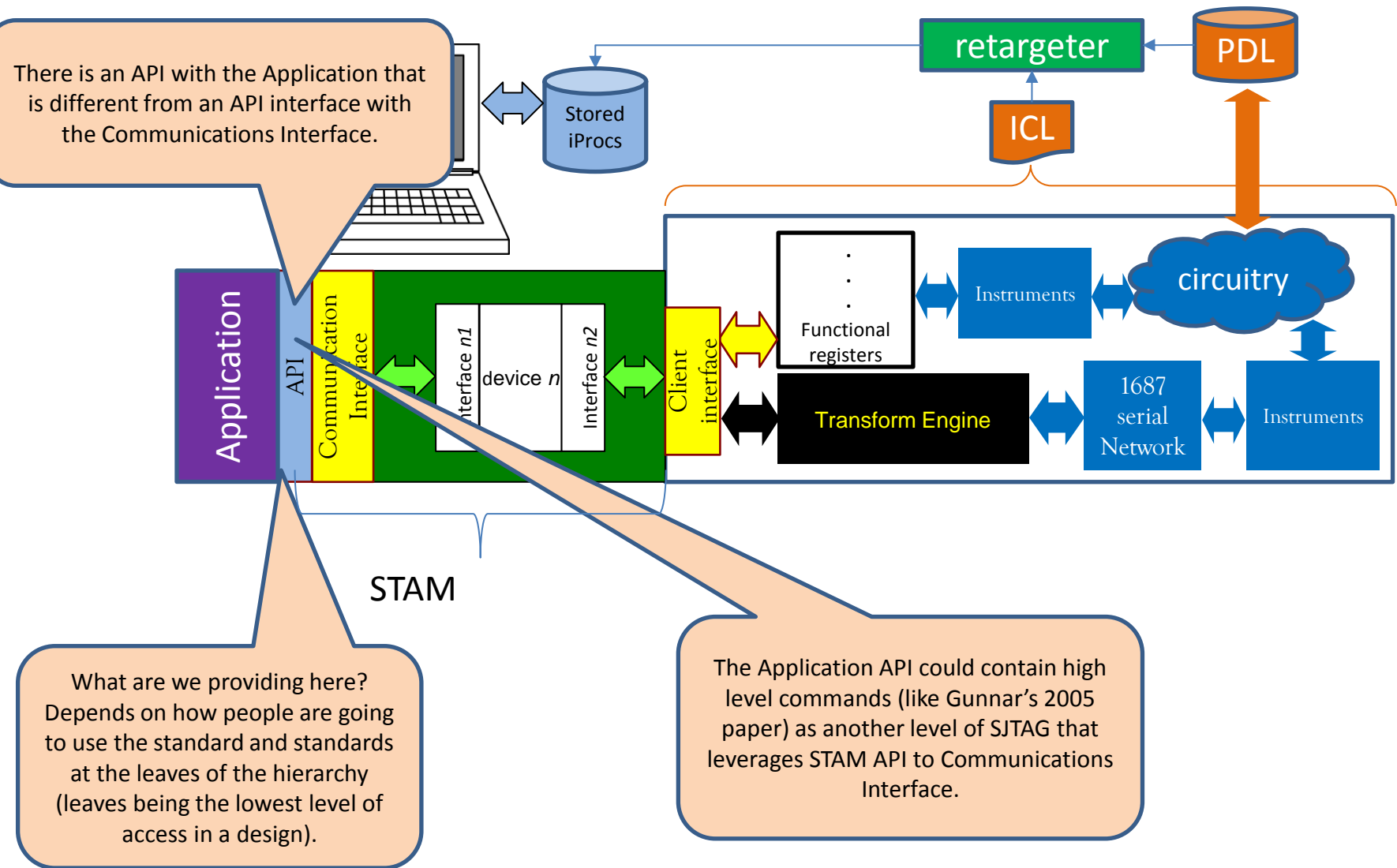


STAM SCOPE?

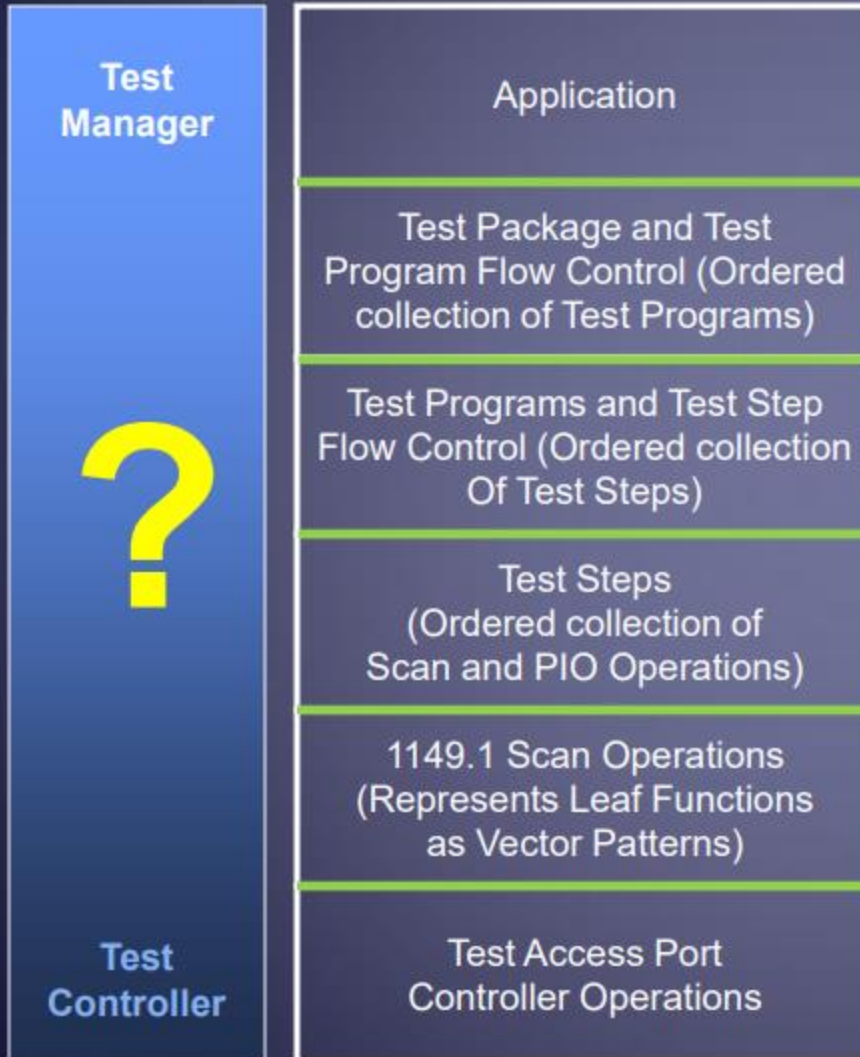


How far left do we go with STAM?
Application is perhaps outside STAM scope. Stopping at the API seems appropriate.

What should API be used for?



Layers of Software

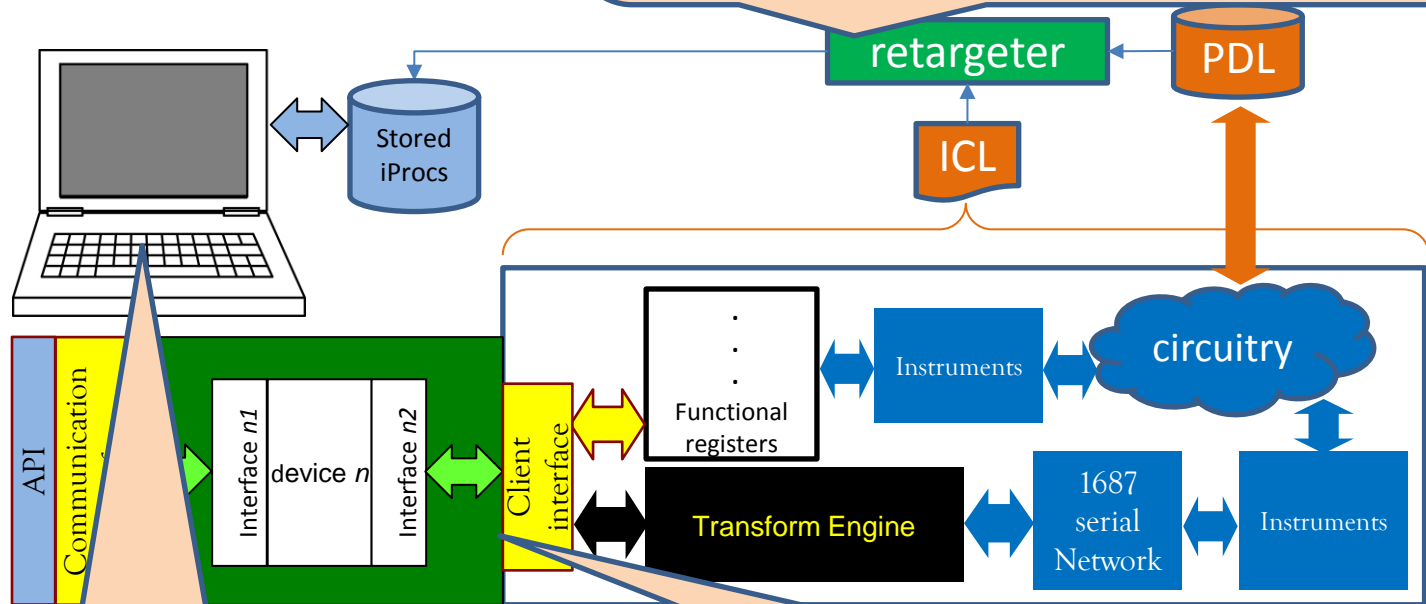


- Avoid thinking in terms of “hardware”:
 - Capabilities may co-reside within the same hardware or may not but will retain their own distinct “role” in either case.
- There are defined interface boundaries between each layer.
- **Green** lines show interface boundaries.

Key point showing APIs that are possible

Locality of STAM

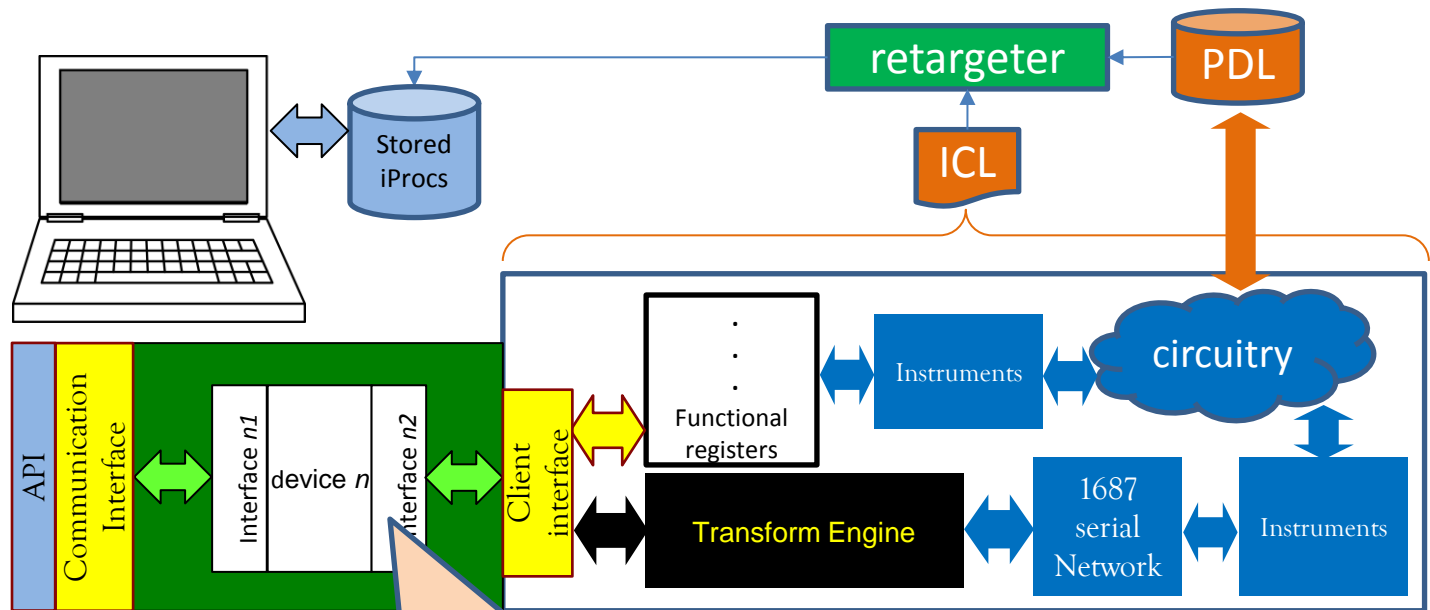
What do you retarget?
It depends on what you are doing for the use case. Just good or not or need data?
It depends on what your application is needing.
Depends on what sub-component is able to provide.
Data may not be singular, but a plurality of information being provided by the instrument (e.g., SERDES data).



Need to reveal that STAM is a software model that in most cases resides off board in the external test PC, but may reside as part of the board application software for interactive embedded applications.

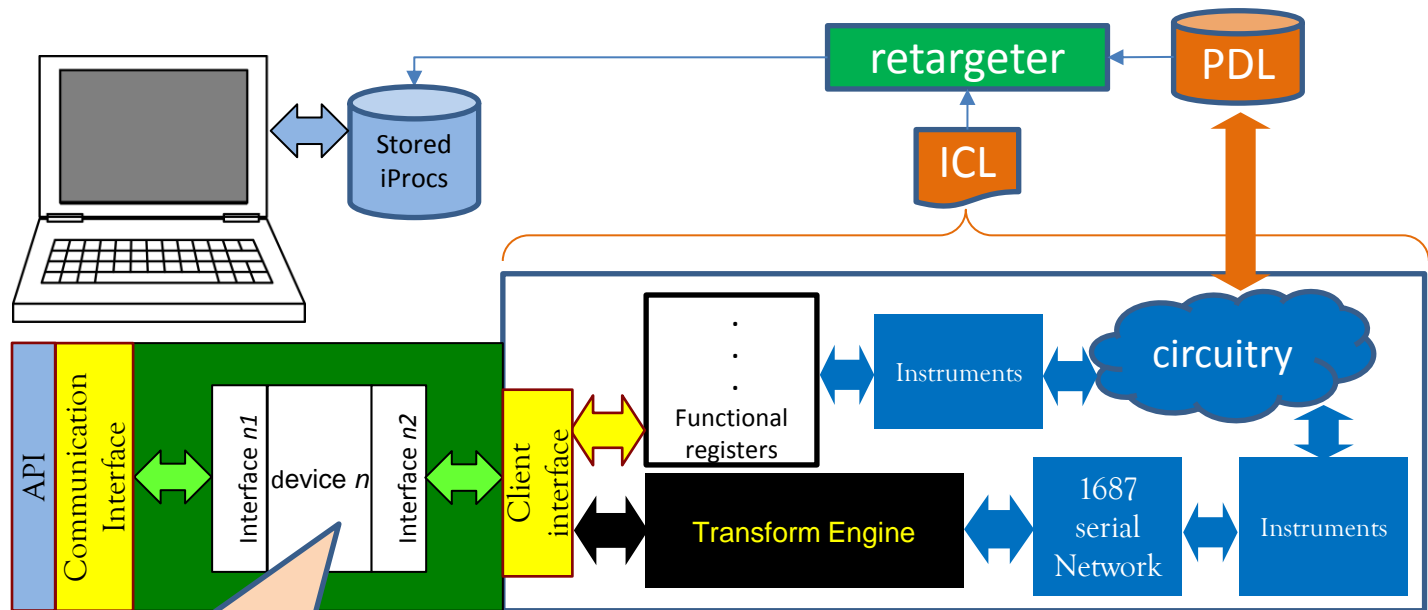
What is provided at the Client Interface for STAM to use?
Is it retargeted PDL scoped to DPIC level commands?
Is it access directives for each of the registers needing to be accessed by the Application?
Or what? What is the level of information required to be compliant vs. compatible? At a minimum acknowledge command was received.

Missing description for Host Interface



Standards are not describing what interface n2 does. iNEMI BM-BIST group identified description templates to show this information. 1687 and 1149.1-2013 only describe how to control the left side of interface n2 and not what n2 is used for. STAM may need to provide a description of what n2 is used for as part of its descriptions.

Multiple instances of retargeters



STAM may need to coordinate multiple instances of a retargeter (possibly from a different vendor than retargeter used for the right hand side of the document) to retarget actions inside device n for controlling interface n2 from interface n1 (e.g., JTAG to I2C bridge application).