Draft Purpose

STAM Purpose:

- The purpose of this standard is to provide a means to seamlessly integrate component access topologies ~~(that follow a Capture, Shift, Update cycle)~~, interface constraints, and other dependencies at the board and system level by using a uniform description that focuses on topology and behavior (as opposed to physical structure). By modeling this topology at the board and system level, a set of familiar and yet interchangeable interfaces may be used by higher level tools to coordinate these access topologies and provide a means of routing data sets to particular destination registers in the correct time order.

- Need to ensure destination register is explicit
- Destination register is part of the leveraged standard interface. STAM ends at the interface and not the register.
- There is an implicit assumption that all entities of the system can be represented in a digital form for measuring.
- Need caution when dealing with access to lower level interfaces as the interface may not be a standard interface.
- Part of purpose is to provide a uniform way to define and access various interfaces. This ties in with SJTAG as we use the data register as the intermediary register to manipulate what goes into the register from various interfaces like we use 1149.1 to get into test data registers.