

Draft Purpose

STAM Purpose (16 April proposal):

- The purpose of this standard is to provide a means to seamlessly integrate component access topologies, interface constraints, and other dependencies at the board and system level by using a uniform description that focuses on topology, **interfaces** and behavior (as opposed to physical structure). **This standard will include a methodology to ensure access to particular destination registers in the correct time order.**
- Questions raised by Adam Ley:
 - what manner of "thing(s)" would claim conformance to the standard? (In Scope?)
 - who produces this(these) thing(s)? (In Purpose?)
 - who uses this(these) thing(s)? (In Purpose?)