

Draft Purpose

STAM Purpose (14 May proposal):

- The purpose of this standard is to provide a means to seamlessly integrate component access topologies, interface constraints, and other dependencies at the board and system level by using a **uniform standardized descriptions** ~~that focuses~~ focusing on topology, **interfaces** and behavior (as opposed to physical structure). **This will ease the burden on those preparing test, maintenance and support applications, including ATPGs, in particular where the application requires to co-ordinate control of and data transfer through multiple interfaces and/or protocols. The providers of these conforming descriptions are the producers of ~~people who produce~~ digital devices or assemblies with digital interfaces that are intended to be used in an automated fashion within a larger assembly.** This standard will include a methodology to ensure access to particular destination registers in the correct time order.